IN THE SPECIFICATION:

Please insert the following new paragraph and corresponding heading before the paragraph and corresponding heading beginning at page 1, line 4.

STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT

This invention was made with United States Government support under MDA904-96-C-0048 with the National Security Agency (NSA). The United States Government may have certain rights in the invention.

Please insert the following new paragraph before the paragraph beginning at page 13, line 13.

In at least some embodiments, the encryption data is stored in non-volatile memory. In at least some of these instances, the non-volatile memory can include a portion internal to the integrated circuit chip and a portion external to the integrated circuit chip, where the encrypted data is stored on the portion internal to the integrated circuit chip when the portion internal is available.

Please substitute the following replacement paragraph for the paragraph beginning at page 14, line 30.

In step 312, the sensitive information that is temporarily stored in the zeroizable memory is erased. This is accomplished by the ~~hose~~ host processor overwriting the information stored in the secure memory or by activating or asserting a zeroize input to the zeroizable memory. In accordance with this embodiment of the present invention, there is no need to re-encrypt the sensitive information or store this information back into external memory because preferably, the original encrypted sensitive information is not removed from external memory and is still contained within the external memory to be used next time the host processor requires access to the sensitive information.